

L Number	Hits	Search Text	DB	Time stamp
1	417930	threshold	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/08/05 12:09
2	4335	(slice or slicing) adj level	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/08/05 13:19
3	421518	threshold or ((slice or slicing) adj level)	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/08/05 12:49
4	42888	coarse with fine	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/08/05 12:49
5	36	((slice or slicing) adj level) and (coarse with fine)	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/08/05 12:49
6	252	(threshold or ((slice or slicing) adj level)) with (coarse with fine)	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/08/05 12:52
7	535	(threshold or ((slice or slicing) adj level)) same (coarse with fine)	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/08/05 12:53
8	408	375/317.ccls.	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/08/05 12:53
9	1	((threshold or ((slice or slicing) adj level)) with (coarse with fine)) and 375/317.ccls.	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/08/05 13:14
10	2209438	speed or fast	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/08/05 13:14
11	76593	adaptive	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/08/05 13:15
12	1221	adaptive adj (threshold or ((slice or slicing) adj level))	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/08/05 13:15
13	115	(speed or fast) same (adaptive adj (threshold or ((slice or slicing) adj level)))	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/08/05 13:19
14	1116737	adjust??	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/08/05 13:19
15	60	((slice or slicing) adj level) near2 adjust??	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/08/05 13:20
-	148	375/317.ccls. and average	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/08/04 14:09
-	68	375/317.ccls. and (average with (threshold or slicing or slice))	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/08/04 15:56





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**1 A 1.2-GIPS/W microprocessor using speed-adaptive threshold-voltage CMOS with forward bias**

Miyazaki, M.; Ono, G.; Ishibashi, K.;  
Solid-State Circuits, IEEE Journal of, Volume: 37 , Issue: 2 , Feb. 2002  
Pages:210 - 217

[Abstract] [PDF Full-Text (193 KB)] IEEE JNL

**2 A 1000-MIPS/W microprocessor using speed adaptive threshold-voltage CMOS with forward bias**

Miyazaki, M.; Ono, G.; Hattori, T.; Shiozawa, K.; Uchiyama, K.; Ishibashi, K.;  
Solid-State Circuits Conference, 2000. Digest of Technical Papers. ISSCC. 2000  
IEEE International , 7-9 Feb. 2000  
Pages:420 - 421, 475

[Abstract] [PDF Full-Text (295 KB)] IEEE CNF

**3 A delay distribution squeezing scheme with speed-adaptive threshold-voltage CMOS (SA-Vt CMOS) for low voltage LSIs**

Miyazaki, M.; Mizuno, H.; Ishibashi, K.;  
Low Power Electronics and Design, 1998. Proceedings. 1998 International Symposium on , 10-12 Aug. 1998  
Pages:48 - 53

[Abstract] [PDF Full-Text (388 KB)] IEEE CNF

**4 Adaptive control of link adaptation for high speed downlink packet access (HSDPA) in W-CDMA**

Nakamura, M.; Awad, Y.; Vadgama, S.;  
Wireless Personal Multimedia Communications, 2002. The 5th International Symposium on , Volume: 2 , 27-30 Oct. 2002  
Pages:382 - 386 vol.2

[Abstract] [PDF Full-Text (490 KB)] IEEE CNF

**5 Adaptive characterization of jitter noise in sampled high-speed signals**

Coakley, K.J.; Wang, C.-M.; Hale, P.D.; Clement, T.S.;

Instrumentation and Measurement, IEEE Transactions on , Volume: 52 , Issue: 5 , Oct. 2003  
Pages:1537 - 1547

[Abstract] [PDF Full-Text (509 KB)] IEEE JNL

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**6 Design and implementation of real-time 3-D image sensor with 640 /spl times/ 480 pixel resolution**

*Oike, Y.; Ikeda, M.; Asada, K.;*  
Solid-State Circuits, IEEE Journal of , Volume: 39 , Issue: 4 , April 2004  
Pages:622 - 628

[Abstract] [PDF Full-Text (576 KB)] IEEE JNL

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**7 Modified fast climbing search auto-focus algorithm with adaptive step size searching technique for digital camera**

*Jie He; Rongzhen Zhou; Zhiliang Hong;*  
Consumer Electronics, IEEE Transactions on , Volume: 49 , Issue: 2 , May 2003  
Pages:257 - 262

[Abstract] [PDF Full-Text (427 KB)] IEEE JNL

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**8 A single-chip Viterbi decoder for a binary convolutional code using an adaptive algorithm**

*Wen-Ta Lee; Ming-Hwa Chan; Liang-Gee Chen; Mao-Chao Lin;*  
Consumer Electronics, IEEE Transactions on , Volume: 41 , Issue: 1 , Feb. 1995  
Pages:150 - 159

[Abstract] [PDF Full-Text (556 KB)] IEEE JNL

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**9 Adaptive nonlinear cancellation for high-speed fiber-optic systems**

*Winters, J.H.; Kasturia, S.;*  
Lightwave Technology, Journal of , Volume: 10 , Issue: 7 , July 1992  
Pages:971 - 977

[Abstract] [PDF Full-Text (672 KB)] IEEE JNL

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**10 ACMOS: an adaptive CMOS high performance logic**

*Kumar, R.;*  
Electronics Letters , Volume: 30 , Issue: 6 , 17 March 1994  
Pages:483 - 484

[Abstract] [PDF Full-Text (168 KB)] IEE JNL

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**11 Novel predictive power control in a CDMA mobile radio system**

*Lau, F.C.M.; Tam, W.M.;*  
Vehicular Technology Conference Proceedings, 2000. VTC 2000-Spring Tokyo. 2000  
IEEE 51st , Volume: 3 , 15-18 May 2000  
Pages:1950 - 1954 vol.3

[Abstract] [PDF Full-Text (408 KB)] IEEE CNF

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**12 Comparison of QRS peak detection algorithms in extracting HRV signal**

*de Jesus Gomez, M.; Sierra, G.; Helie, F.; Rocque, P.; Vinet, A.; Cardinal, R.; Nadeau, R.;*  
Engineering in Medicine and Biology society, 1997. Proceedings of the 19th Annual International Conference of the IEEE , Volume: 1 , 30 Oct.-2 Nov. 1997  
Pages:306 - 307

[Abstract] [PDF Full-Text (256 KB)] IEEE CNF

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**13 High speed and nonvolatile Si nanocrystal memory for scaled flash technology using highly field-sensitive tunnel barrier**

*Seung Jae Baik; Siyoung Choi; U-In Chung; Joo Tae Moon;*  
Electron Devices Meeting, 2003. IEDM '03 Technical Digest. IEEE International , 8-10 Dec. 2003  
Pages:22.3.1 - 22.3.4

[Abstract] [PDF Full-Text (340 KB)] IEEE CNF

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**14 640/spl times/480 Real-time range finder using high-speed readout scheme and column-parallel position detector**

*Oike, Y.; Ikeda, M.; Asada, K.;*  
VLSI Circuits, 2003. Digest of Technical Papers. 2003 Symposium on , 12-14 June 2003  
Pages:153 - 156

[Abstract] [PDF Full-Text (404 KB)] IEEE CNF

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**15 Adaptive rate control in high data rate wireless networks**

*Yavuz, M.; Paranchych, D.W.;*  
Wireless Communications and Networking, 2003. WCNC 2003. 2003 IEEE , Volume: 2 , 16-20 March 2003  
Pages:866 - 871 vol.2

[Abstract] [PDF Full-Text (417 KB)] IEEE CNF

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